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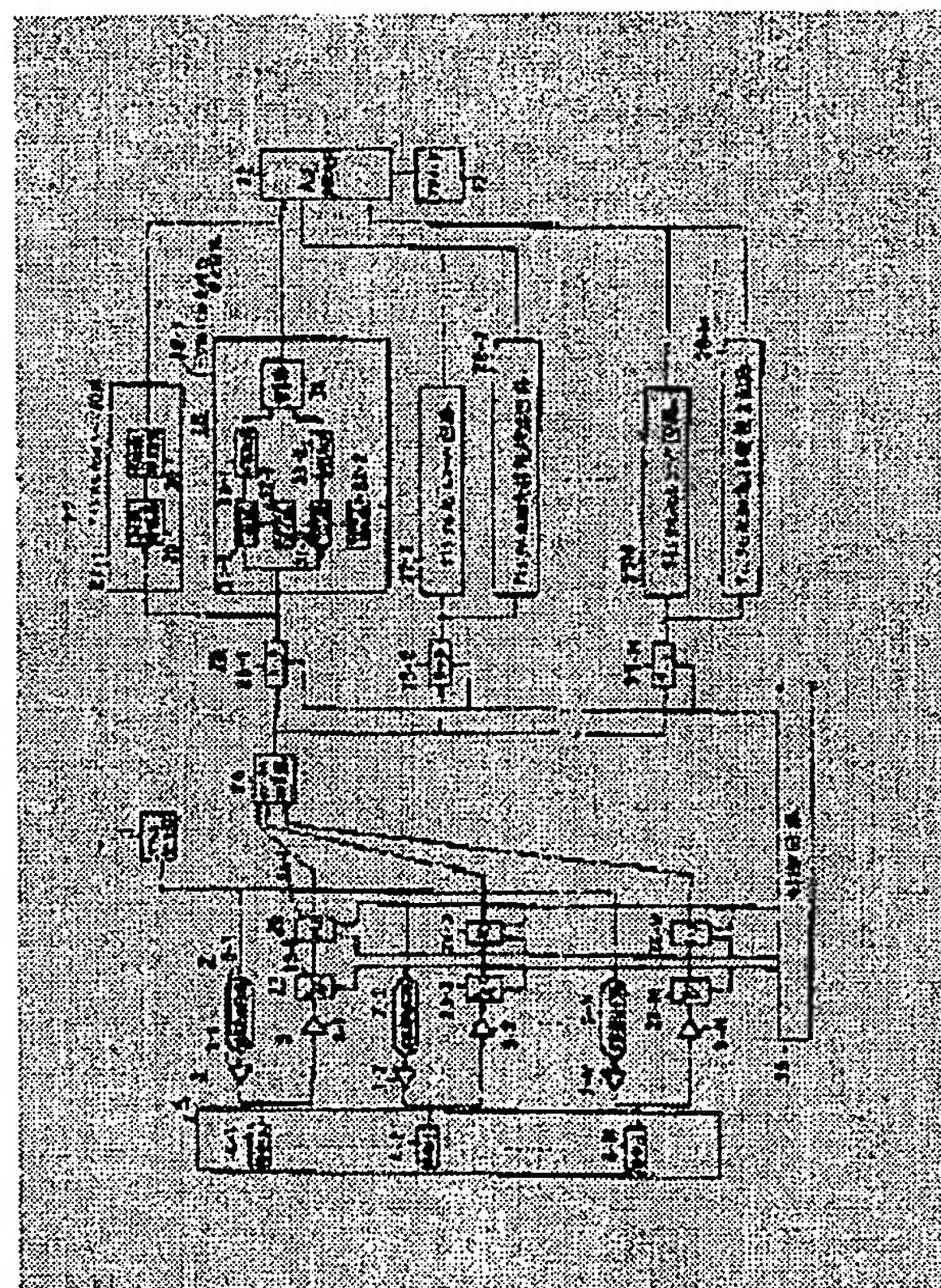
ULTRASONIC DIAGNOSTIC APPARATUS

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Abstract of JP2206445

PURPOSE: To establish a multi-direction simultaneous receiving system by suppressing an increase of a circuit scale as low as possible by respectively providing memory means storing ultrasonic echo data to a multi-channel receiving delay circuit and mounting a control means for setting the data reading speed to each of the memory means to a data writing speed or more. **CONSTITUTION:** The digitalized echo data of each channel is written in a memory 24 (24-1)-(24-N) using a writing address generated by a clock of frequency f_w . Next, data is read from the memory 24, that is (24-1)-(24-N) by the independent reading address of each channel generated of a clock of frequency $f_{RE} = M \cdot f_w$. By this method, the ultrasonic echo data of each channel obtained by one transmission and reception of an ultrasonic wave can be read by giving a delay time for obtaining M-kinds of receiving directionalities and the echo data of all channels are added by a digital adder 26 and separated by a latch 25, that is (25-a)-(25-M) to make it possible to obtain the receiving signals corresponding to M-kinds of the receiving directionalities.



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